



Zulkifli Md Yusof

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## Academic Qualification

### **Master of Science in Electrical Engineering(1993)**

Washington State Univesity, Pullman, Washington, USA

### **Bachelor of Science in Electrical Engineering (1989)**

University of Arizona, Tucson, Arizona, USA

## Brief Profile

My primary research interests lie in the fields of Microelectronics and Computer Engineering. I am also interested in investigation of optimization algorithm in VLSI Routing, PCB Drilling and Assembly Sequence Planning. The list of interests is stated below:

- Compressed Memory Architecture
- VLSI Routing
- Optimization Algorithm..

## Research Project

FRGS/2/10/SG/UTM/02/24, A New Multi-State Particle Swarm Optimization For Discrete Combinatorial Optimization Problems, (January 2011 – December 2012) **Project Leader**

FRGS/2/10/SG/UTM/03/12, New Fitness Functions For Dna Sequence Design With Arbitrary Length (December 2010 – October 2012) **Researcher**

FRGS/1/10/SG/UTM/03/10 (ST). An Improved Vector Evaluated Particle Swarm Optimization (Vepso) To Achieve Good Quality Of Pareto Frontier For Multi-Objective Optimization (Mac 2010 – Feb. 2012) **Researcher**

eScience , Project No.: 01-01-06-SF0663 Vot 79361 : MOSTI : A Particle Swarm Optimization Approach For Vlsi Routing (April 2009 –October 2010 ) **Project Leader**

FRGS, Vot 78031 , Ministry of Higher Education : CAD Algorithm & Architecture in Physical Design System for Nano-scale VLSI Intergrated Circuits 2007, (Julai 2006 – Julai 2008) **Researcher**

## Teaching / Working Experience

Senior Lecturer | Universiti Malaysia Pahang | August 2013 – Present [Teaching courses: Power Electronics and Analog Electronics]

Senior Lecturer | Universiti Teknologi Malaysia | May 2010 – August 2013 [Teaching courses: Electronic Computer Aided Design, VLSI Design, Digital System Design, and Digital Electronics]

Lecturer | Universiti Teknologi Malaysia | May 1996 – May 2010 [Teaching courses: Electronic Device, Electronic Circuits and Electronic System]

Asistant Lecturer | Universiti Teknologi Malaysia | December 1989 – May 1996 [Teaching courses: Electrical Engineering, Computer Architecture and Microelectronics]

Assembly Engineer | Hitachi Semiconductor Penang | August 1989 – October 1989